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READ ONLY MEMORIES

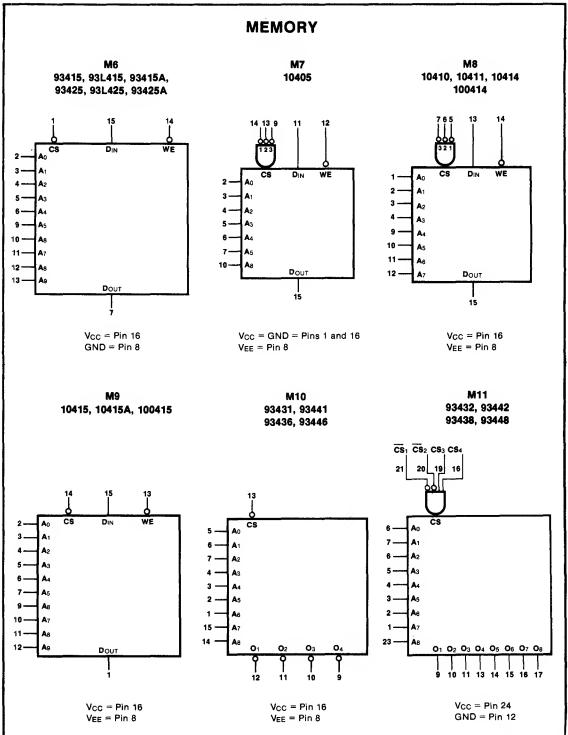
BIPOLAR ROMS AND PROMS

Item	Organization	DEVICE NO.	Description ⁽¹⁾	Address Access Time ns (Typ)	Chip Select Access Time ns (Typ)	Read Cycle Time				
						0°C to +70°C ns (Max)	-55°C to +125°C ns (Max)	Power Dissipation mW (Typ)	Logic/Connection Diagram	Package(s)
TTL				1						
1	16x48x8	93458	FPLA,OC	25	15	_		750	M20	8E,9Y
2	16x48x8	93459	FPLA,3S	25	15			750	M20	8E,9Y
3	256x4	93457	ROM,OC	25	12	45	60	425	M17	3D,6D,9B
4	256x4	93467	ROM,3S	25	12	45	60	425	M17	3D,6D,9B
5	256x4	93417	PROM,OC	25	12	45	60	425	M17	3D,6D,9B
6	256x4	93427	PROM,3S	25	12	45	60	425	M17	3D,6D,9B
7	512x4	93436	PROM,OC	30	15	50	60	475	M10	3D,6D,9B
8	512x4	93446	PROM,3S	30	15	50	60	475	M10	3D,6D,9B
9	512x4	93431	ROM,OC	30	15	50	60	475	M10	48,6D,9B
10	512x4	93441	ROM,3S	30	15	50	60	475	M10	48,6D,9B
11	512x8	93432	ROM,OC	35	15	55	70	650	M11	4R,6M,7L,9N
12	512x8	93442	ROM,3S	35	15	55	70	650	M11	4R,6M,7L,9N
13	512x8	93438	PROM,OC	35	15	55	70	650	M11	4R,6M,7L,9N
14	512x8	93448	PROM,3S	35	15	55	70	650	M11	4R,6M,7L,9N
15	1024x4	93452	PROM,OC	30	15	55	70	650	M18	8F,9M
16	1024x4	93453	PROM,3S	30	15	55	70	650	M18	8F,9M
17	1024x8	93450	PROM,OC	30	20	45	60	550	M21	4R,6M,9N
18	1024x8	93451	PROM,3S	30	20	45	60	550	M21	4R,7L,9N
19	1024x8	93454	ROM,OC	30	20	45	60	550	M12	4R,6M,7L,9N
20	1024x8	93464	ROM,3S	30	20	45	60	550	M12	4R,6M,7L,9N
ECL										
21	256x4	10416	PROM	15	4.0	25 ⁽²⁾		650	M19	4B,6D
22	256x4	100416	PROM	15	4.0	25 ⁽²⁾	_	650	M19	4B,6D

OC = open collector, 3S = 3-state
-30°C to +85°C



FAIRCHILD LOGIC/CONNECTION DIAGRAMS



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